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6/11/01**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Joseph CHEN et al.

INTEGRATED CIRCUIT DEVICE HAVING A  
CORE CONTROLLER, A BUS BRIDGE, A  
GRAPHICAL CONTROLLER AND A UNIFIED  
MEMORY CONTROL UNIT BUILT THEREIN  
FOR USE IN A COMPUTER SYSTEM

Appl. No.: 09/199,478

Filing Date: November 25, 1998

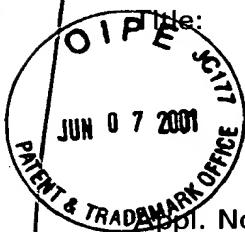
Examiner: Kee M Tung

Art Unit: 2671

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Technology Center 2600

**REPLY UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
Box NON-FEE AMENDMENT  
Washington, D.C. 20231

Sir:

This communication is responsive to the Office Action dated March 7, 2001  
concerning the above-referenced patent application.

Please amend the application as follows:

**IN THE CLAIMS:**

Please amend the claims with the following amended claims. Marked up copies  
showing changes made are attached.

1. (Amended) An integrated circuit device for use in a computer system that includes a processing unit, a host bus connected to the processing unit, an input/output bus, a peripheral device connected to the input/output bus, a monitor, and a system memory, said integrated circuit device comprising:

a core controller adapted to be connected to the host bus;

a bus bridge connected to said core controller and adapted to be connected to the input/output bus;